((w)) wasiela

**Wireless Advanced System
Innovations and Electronics Art**

Winning Through Creativity

www.wasiela.com

Overview

- IP cores for digital media solutions, and wireless digital communications physical layer design.

- High performance, power efficient, configurable Microcoded application specific instruction set (ASIP) cores.

- Efficient highly performing algorithms carefully designed to yield ultra low power, highly configurable designs.

- Core team started Semiconductor work in 1990. Our experience was acquired at Bell Labs, Texas Instruments, ADI, IBM, AMD and Intel – 8 PhDs.

- Successful FPGA and ASIC designs market proven.

- Submitted 20 contributions to the WiMAX 802.16m standards committee.

- Published numerous papers including 10 papers on OFDM systems and preparing to file ten patents and several others in development.

Expertise

- Digital IC design (VHDL, Verilog).

- System level modeling and simulations.

- Application specific instruction set processor (ASIP) cores.

- ARM programming.

- PCB design.

- Digital design verification and timing closure.

- Synthesis to FPGA or ASIC.

- Implementation on programmable DSP.

- Testing and qualification.

Design Services
Communications transceiver blocks

- Avaliable:

  - Error Correction
    LDPC, BCH, Turbo Decoder, Reed Solomon, Viterbi

  - Modulation / Demodulation
    FFI / IFFT, OFDM Channel Estimator

- Others: Available upon request

Typical Customer Engagements :

- From specification to silicon.

- Focus on wireless applications.

- Work with partners to provide full solution.

- Support and training at your site.

Activities focused on Broadband Physical Layer implementations and FEC cores

Including ARM programming